

Anish Vivek

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EDUCATION

Bachelor of Science, Electrical and Computer Engineering, Graduation May 2029

The University of Texas at Austin

Relevant Coursework: ECE306, ECE302, ECE319H, ECE312H

Overall GPA: 3.69/4.00 SAT:1500

WORK EXPERIENCE

Biotechnology Intern, Austin Community College

07/2024 – 12/2024

- Worked alongside the Austin Community College, working on the genetic sequencing lab of fauna throughout Austin, and helped with the identification of proteins and genetic sequences within bacteriophages.

Instructor, I-Code Austin

05/2024 – 6/2025

- Tutored children in coding fundamentals, fostering interest in STEM fields, developed lesson plans and interactive coding exercises to enhance learning.

Rackspace Technology– Strategy Analyst

08/2025-11/2025

- Conducted financial, valuation, and industry analysis to assess Rackspace Technology's performance and strategic market position
- Built a discounted cash flow (DCF) model to evaluate enterprise value and key value drivers under multiple potential scenarios
- Synthesized financial findings and competitive landscape insights into recommendations and presented to the Chief Strategy Officer

ACADEMIC PROJECTS

Lookup Arithmetic Logic Unit

01/2025 – 05/2025

- Designed and implemented a Look-Ahead Logic Unit (LALU) in Logisim, optimizing binary addition performance by reducing propagation delay compared to ripple-carry adders.
- Applied digital logic design principles (Boolean algebra, Karnaugh maps, and hierarchical circuit construction) to develop, test, and verify the functionality of the LALU through simulation.

Buck Converter

11-2025-12/2025

- Designed and analyzed a DC-DC buck converter, deriving steady-state equations and evaluating output voltage regulation, inductor current ripple, and efficiency under varying load conditions.

NASA SPARX Moon Landing Coding Challenge

11/2021-02/2022

- Collaborated with a team to develop coding solutions for simulating moon landing scenarios, applying problem-solving and algorithmic design under time constraints.
- Implemented Python programs to convert Cartesian inputs into 3D (X, Y, Z) coordinate representations, supporting trajectory modeling and lunar landing calculations.

SKILLS

Programming: Python (6+ years), Java,

Databases: MS Access, SQL (complex queries, reporting)

Microprocessors: Digital/logic circuit design, ARM & assembly programming

Digital Systems: Binary/hex systems, CMOS logic, Boolean algebra, Karnaugh Maps

Design: KiCad, PCB Design, and Implementation

ACCOMPLISHMENTS

2nd Place in Freshman Engineering Case Competition, NASA SPARX Moon Landing Challenge Winner; FBLA Database Design National Qualifier; CyberPatriot National Platinum Semifinalist (Top 100), Student Council President; National Honor Society Liaison; FBLA Officer; CyberPatriot Team Member