Anish Vivek

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EDUCATION

Bachelor of Science, Electrical and Computer Engineering, Graduation May 2029
The University of Texas at Austin
Relevant Coursework: ECE306, ECE302, ECE319H, ECE312H
Overall GPA: 3.69/4.00 SAT:1500

WORK EXPERIENCE

Biotechnology Intern, Austin Community College **07/2024 – 12/2024**

- Worked alongside the Austin Community College, working on the genetic sequencing lab of fauna throughout Austin, and helped with the identification of proteins and genetic sequences within bacteriophages.

Instructor, I-Code Austin **05/2024 – 6/2025**

- Tutored children in coding fundamentals, fostering interest in STEM fields, developed lesson plans and interactive coding exercises to enhance learning.

Rackspace Technology– Strategy Analyst **08/2025-11/2025**

- Conducted financial and industry analysis of Rackspace Technology and built a discounted cash flow (DCF) model to evaluate enterprise value, presenting insights and recommendations to the Chief Strategy Officer.

ACADEMIC PROJECTS

Lookup Arithmetic Logic Unit **01/2025 – 05/2025**

- Designed and implemented a Look-Ahead Logic Unit (LALU) in Logisim, optimizing binary addition performance by reducing propagation delay compared to ripple-carry adders.
- Applied digital logic design principles (Boolean algebra, Karnaugh maps, and hierarchical circuit construction) to develop, test, and verify the functionality of the LALU through simulation.

Buck Converter **11-2025-12/2025**

- Designed and analyzed a DC-DC buck converter, deriving steady-state equations and evaluating output voltage regulation, inductor current ripple, and efficiency under varying load conditions.

NASA SPARX Moon Landing Coding Challenge **11/2021-02/2022**

- Collaborated in the NASA SPARX Moon Landing Coding Challenge to develop Python programs that converted Cartesian inputs into 3D ((X, Y, Z)) coordinates for lunar landing trajectory simulations.

Kalshi Arbitrage Scanner | Python, REST APIs, Algorithmic Trading **1/26 – Present**

- Built a real-time scanner that detects arbitrage opportunities across 3 prediction market platforms (Kalshi, Polymarket, Manifold) by analyzing 500+ live contracts per scan.
- Modeled cross-platform profit margins accounting for per-platform fee structures, implementing optimal bet sizing logic to maximize risk-free returns within a fixed budget constraint.

SKILLS

Programming: Python (6+ years), Java,
Databases: MS Access, SQL (complex queries, reporting)
Microprocessors: Digital/logic circuit design, ARM & assembly programming
Digital Systems: Binary/hex systems, CMOS logic, Boolean algebra, Karnaugh Maps
Design: KiCad, PCB Design, and Implementation, Electrical Tools(Oscilloscope, Soldering,)

ACCOMPLISHMENTS

2nd Place in Freshman Engineering Case Competition, NASA SPARX Moon Landing Challenge Winner; FBLA Database Design National Qualifier; CyberPatriot National Platinum Semifinalist (Top 100), Student Council President; National Honor Society Liaison; FBLA Officer; CyberPatriot Team Member