Abdou Kadri Jallow

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github.com/akjallow

Skills

Software: Python, C++, Tensorflow , Altium Designer, Kicad, LTspice , Autodesk Fusion, Git

Languages: English

Education

Gujarat Technological University, BE in Electrical Engineering – Ahmedabad , Gujarat   Sept 2023 – present
• GPA: 8.85/10.00

Experience

Robotics Intern, MyEquation – Ahmedabad , Gujarat   July 2025 – Sept 2025
• Developed Deep learning algorithms for Robotic control and perception using python and tensorflow.
• Designed and developed 3D robotic models in Fusion 360 , and simulations in ROS2 for SLAM

Electronics Intern, Sofcon India Pvt ltd – Ahmedabad , Gujarat   Jan 2024 – Mar 2024
• Developed digital logic algorithms to control electronic systems using programmable logic controllers.
• Designed and developed manufacturable and industry standard printed circuit boards(PCB) for applications in electronic systems.
• Implemeted various communication protocols such as SPI, I2C, UART for sensor interfacing.

Projects

Smart Grid Forecasting   Sept 2025 – Nov 2025
• Developed a high accuracy hybrid machine learning model using LSTM and GRU deep learning architectures
• Developed a web interface for real-time forecasting of electricity consumptions with an approximate accuracy of 81%.

Prescence Pro   Oct 2025 – present
• Worked in a team of six to develop a real-time attendance system for our college department
• Developed a facial recognition computer vision model using transfer learning.

FPGA based System-on-Module   Dec 2025 – present
• Designing a system-on-module for signal processing applications around the Xilinx7000 series FPGA using Altium Designer.

RobCore   July 2025 – present
• Working on the design and development of an autonomous robot for smart logistics in educational institutions.
• Designed a PCB using altium designer integrating requisite interfaces and sensors for perception , navigation and localization.

Audio Processing Board   Aug 2024 – Sept 2024
• Designed a mixed-signal audio processing board using Kicad.
• Achieved good signal and power integrity by following mixed-signal layout design principles.

Activities

• World Stem Robotics olympiad (2024)

• Smart india Hackathon (2024 ,2025)